This listing of the Claims will replace all prior versions and listing of Claims in the application.

AMENDED CLAIMS:

I CLAIM:

1. (currently amended) ~~An eight bit code read from left to right on at least eight sensors comprising:~~

[[a]] A first four bit code combined with a second four bit code to produce data.

2. (currently amended) ~~An eight bit code read from left to right on at least eight sensors to produce data~~ A first four bit code combined with a second four bit code to produce data, in accordance with claim 1, wherein:

1. a ~~left~~ first bit of said ~~eight~~ first four bit code has the numeric value of one, and

b) a second bit of said ~~eight~~ first four bit code has the numeric value of two, and

c) a third bit of said ~~eight~~ first four bit code has the numeric value of four, and

d) a fourth bit of said ~~eight~~ first four bit code has the numeric value of eight, and

e) a ~~fifth~~ first bit of said ~~eight~~ second four bit code has the numeric value of sixteen, and

f) a ~~sixth~~ second bit of said ~~eight~~ second four bit code has the numeric value of thirty-two, and

g) a ~~seventh~~ third bit of said ~~eight~~ second four bit code has the numeric value of sixty-four, and

h) a ~~right eighth~~ fourth bit of said ~~eight~~ second four bit code has the numeric value of one hundred and twenty-eight.

3. (currently amended) A method of producing data using ~~an eight bit~~ ~~code~~ a first four bit code combined with a second four bit code ~~read from left to right on~~ ~~at least eight sensors~~ comprising the step of:

activating at least one sensor to enter ~~an eight~~ a first four sensor mode combined with a second four sensor ~~data entry~~ mode.

4. (currently amended) A method of producing data using ~~an eight bit~~ ~~code~~ a first four bit code combined with a second four bit code ~~read from left to right on~~ ~~at least eight sensors~~, in accordance with claim 3, comprising the step of:

activating at least one ~~said~~ sensor of ~~said~~ at least eight sensors to enter an eight said first four sensor mode combined with a second four sensor ~~data entry~~ mode.

5. (currently amended) A method of producing data using ~~an eight bit~~ ~~code~~ a first four bit code combined with a second four bit code ~~read from left to right on~~ ~~at least eight sensors~~, in accordance with claim 3, comprising the step of:

activating ~~all said~~ eight sensors to enter ~~an eight sensor data entry~~ said first four sensor mode combined with a second four sensor mode.

6. (currently amended) A method of producing data using ~~an eight bit~~ ~~code~~ a first four bit code combined with a second four bit code ~~read from left to right on~~ ~~at least eight sensors~~, in accordance with claim 3, comprising the step of:

activating at least one ~~said~~ sensor of ~~said eight~~ four first sensors or four second sensors to produce a data character.

7. (currently amended) A method of producing data using ~~an eight bit~~ ~~code~~ a first four bit code combined with a second four bit code ~~read from left to right on~~ ~~at least eight sensors~~, in accordance with claim 3, comprising the step of:

activating at least one ~~said~~ sensor of ~~said eight~~ four first sensors or four second sensors to produce a function.

8. (currently amended) A method of producing data using ~~an eight bit~~ ~~code~~ a first four bit code combined with a second four bit code ~~read from left to right on~~ ~~at least eight sensors~~, in accordance with claim 3, comprising the step of:

activating at least one ~~said~~ sensor of ~~said~~ eight sensors to produce a data character string.

9. (currently amended) A method of using a first four bit code combined with a second four bit code ~~on at least eight sensors~~, in accordance with claim 3, comprising the step of:

activating at least one ~~said~~ sensor of ~~said~~ eight sensors followed by the activating of at least one said sensor of said eight sensors to produce a data character.

10. (currently amended) A method of using a first four bit code combined with a second four bit code ~~on at least eight sensors~~, in accordance with claim 3, comprising the step of:

activating at least one ~~said~~ sensor of ~~said~~ eight sensors followed by the activating of at least one said sensor of said eight sensors to produce a data character string.

11. (currently amended) A method of using a first four bit code combined with a second four bit code ~~on at least eight sensors~~, in accordance with claim 3, comprising the step of:

activating at least one ~~said~~ sensor of a first set of four sensors combined with non-activating a second set of four sensors to produce a vowel.

12. (currently amended) A method of using a first four bit code combined with a second four bit code ~~on at least eight sensors~~, in accordance with claim 3, comprising the step of:

activating at least one ~~said~~ sensor of a first set of four sensors combined with the activating of at least one ~~said~~ sensor of a second set of four sensors to produce a vowel.

13. (currently amended) A method of using a first four bit code combined with a second four bit code ~~on at least eight sensors~~, in accordance with claim 3, comprising the step of:

activating at least one ~~said~~ sensor of a first set of four sensors combined with the activating of at least one ~~said~~ sensor of a second set of four sensors to produce a consonant.

14. (currently amended) A method of using a first four bit code combined with a second four bit code ~~on at least eight sensors~~, in accordance with claim 3, comprising the step of:

non-activating a first set of four sensors combined with the activating of at least one ~~said~~ sensor of a second set of four sensors to produce a space.

15. (currently amended) A method of using a first four bit code combined with a second four bit code ~~on at least eight sensors~~, in accordance with claim 3, comprising the step of:

non-activating a first set of four sensors combined with the activating of at least one ~~said~~ sensor of a second set of four sensors to produce a punctuation mark.

16. (currently amended) A method of using a first four bit code combined with a second four bit code ~~on at least eight sensors~~, in accordance with claim 3, comprising the step of:

activating at least one ~~said~~ sensor of a first set of four sensors combined with the activating of at least one ~~said~~ sensor of a second set of four sensors to produce a symbol.

17. (currently amended) A method of using a first four bit code combined with a second four bit code ~~on at least eight sensors~~, in accordance with claim 3, comprising the step of:

activating at least one ~~said~~ sensor of a first set of four sensors combined with the activating of all ~~said~~ sensors of a second set of four sensors to produce a number.

18. (currently amended) A method of using a first four bit code combined with a second four bit code ~~on at least eight sensors~~, in accordance with claim 3, comprising the step of:

activating at least one ~~said~~ sensor of a first set of four sensors combined with the activating of all but one sensor of a second set of four sensors to produce a function.

19. (currently amended) An apparatus for entering ~~an eight bit code~~ a first four bit code combined with a second four bit code ~~read from left to right~~ on at least eight sensors wherein:

a) said first four bit code has a first sensor ~~left bit has the~~ numeric value of one ~~and is a left digit sensor~~, and

b) said first four bit code has a second sensor ~~bit has the~~ numeric value of two ~~and is a left digit sensor~~, and

c) said first four bit code has a third sensor ~~bit has the~~ numeric value of four ~~and is a left digit sensor~~, and

d) said first four bit code has a fourth sensor ~~bit has the~~ numeric value of eight ~~and is a left digit sensor~~, and

e) said second four bit code has a fifth sensor ~~bit has the~~ numeric value of sixteen ~~and is a right digit sensor~~, and

f) said second four bit code has a sixth sensor ~~bit has the~~ numeric value of thirty-two ~~and is a right digit sensor~~, and

g) said second four bit code has a seventh ~~bit has the~~ numeric value of sixty-four ~~and is a right digit sensor~~, and

h) said second four bit code has an [[a]] eighth sensor ~~right bit has the~~ numeric value of one hundred and twenty-eight ~~and is a right digit sensor~~.

20. (currently amended) A method of ~~entering an eight bit code read from left to right~~ movement on at least eight sensors comprising the step of:

a)   activating ~~one said left digit sensor moves~~ a first sensor of said eight sensors to move an object in a first direction, and

b)   activating ~~one said right digit sensor moves~~ a second sensor of said eight sensors to move said object in a second opposite direction.

21. (currently amended) A method of ~~entering an eight bit code read from left to right~~ movement on at least eight sensors, in accordance with claim 20, comprising the step of:

a)   activating ~~one said left digit sensor moves~~ said first sensor of said eight sensors to move an object to the left, and

b)   activating ~~one said right digit sensor moves~~ said second sensor of said eight sensors to move said object to the right.

22. (currently amended) A method of ~~entering an eight bit code read from left to right~~ movement on at least eight sensors, in accordance with claim 20, comprising the step of:

a)   activating ~~one said left digit sensor rotates~~ said first sensor of said eight sensors to rotate an object to the left, and

b)   activating ~~one said right digit sensor rotates~~ said second sensor of said eight sensors to rotate said object to the right.

23. (currently amended) A method of ~~entering an eight-bit code read from~~ ~~left to right~~ movement on at least eight sensors, in accordance with claim 20, comprising the step of:

a)   activating ~~one said left digit sensor moves~~ said first sensor of said eight sensors to move an object backward, and

b)   activating ~~one said right digit sensor moves~~ said second sensor of said eight sensors to move said object forward.

24. (currently amended) A method of ~~entering an eight-bit code read from~~ ~~left to right~~ movement on at least eight sensors, in accordance with claim 20, comprising the step of:

a)   activating ~~one said left digit sensor moves~~ said first sensor of said eight sensors to move an object to the down, and

b)   activating ~~one said right digit sensor moves~~ said second sensor of said eight sensors to move said object up.

25. (currently amended) A method of ~~entering an eight-bit code read from~~ ~~left to right~~ movement on at least eight sensors, in accordance with claim 20, comprising the step of:

activating ~~one said left digit sensor and one said right digit sensor~~ said first sensor and said second sensor of said eight sensors simultaneously moves an object forward.

26. (currently amended) A method of ~~entering an eight bit code read from left to right~~ movement on at least eight sensors, in accordance with claim 20, comprising the step of:

activating ~~one said left digit sensor and one said right digit sensor~~ said first sensor and said second sensor of said eight sensors simultaneously followed by activating ~~one said left digit sensor and one said right digit sensor~~ said first sensor and said second sensor of said eight sensors simultaneously moves an object backward.

27. (currently amended) An apparatus for entering an eight bit code read from left to right on at least eight sensors, in accordance with claim 19, wherein:

a) ~~a first left bit has the numeric value of one and~~ said first sensor of said first four bit code is a left digit sensor, and

b) ~~a second bit has the numeric value of two and~~ said second sensor of said first four bit code is a left digit sensor, and

c) ~~a third bit has the numeric value of four and~~ said third sensor of said first four bit code is a left digit sensor, and

d) ~~a fourth bit has the numeric value of eight and~~ said fourth sensor of said first four bit code is a left ~~thumb~~ digit sensor, and

e) ~~a fifth bit has the numeric value of sixteen and~~ said first sensor of said second four bit code is a right ~~thumb~~ digit sensor, and

f) ~~a sixth bit has the numeric value of thirty-two and~~ said second sensor of said second four bit code is a right digit sensor, and

g) ~~a seventh bit has the numeric value of sixty-four and~~ said third sensor of said second four bit code is a right digit sensor, and

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h)   ~~a eighth right bit has the numeric value of one hundred and twenty-eight and~~ said

fourth sensor of said second four bit code is a right digit sensor.

28. (currently amended) A method of ~~entering an eight bit code read from~~

~~left to right~~ movement on at least eight sensors, in accordance with claim 20,

comprising the step of:

a)   activating ~~a left thumb sensor moves~~ said first sensor of said eight sensors to

move the cursor to the left, and

b)   activating ~~a right thumb sensor moves~~ said second sensor of said eight sensors

to move said cursor to the right.

29. (currently amended) A method of ~~entering an eight bit code read from~~

~~left to right~~ movement on at least eight sensors, in accordance with claim 20,

comprising the step of:

a)   activating ~~a left thumb sensor deletes~~ said first sensor of said eight sensors to

delete data to the left of the cursor, and

b)   activating ~~a right thumb sensor deletes~~ said second sensor of said eight sensors

to delete data to the right of said cursor.

30. (currently amended) A method of ~~entering an eight bit code read from~~

~~left to right~~ movement on at least eight sensors, in accordance with claim 20,

comprising the step of:

a)   activating ~~a left thumb sensor reverses~~ said first sensor of said eight sensors to

reverse the last change, and

b)   activating ~~a right thumb sensor reverses~~ said second sensor of said eight

sensors to reverse the last undo.

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31. (currently amended) A method of ~~entering an eight bit code read from~~ ~~left to right~~ underline movement on at least eight sensors, in accordance with claim 20, comprising the step of:

a)   activating ~~a left thumb sensor and a right thumb sensor~~ said first sensor and said second sensor of said eight sensors simultaneously exits ~~said first~~ a data entry mode and enters a cursor movement mode, and

b)   activating said ~~left thumb sensor~~ first sensor of said eight sensors moves ~~the~~ a cursor to the left and activating said ~~right thumb sensor~~ second sensor of said eight sensors moves said cursor to the right; and

c)   activating said ~~left thumb sensor and a right thumb sensor~~ first sensor and said second sensor of said eight sensors simultaneously exits said cursor movement mode and enters a ~~delete~~ deletion mode, and

d)   activating said ~~left thumb sensor deletes~~ first sensor of said eight sensors to delete data to the left of said cursor and activating said ~~right thumb sensor~~ ~~deletes~~ second sensor of said eight sensors to delete data to the right of said cursor, and

e)   activating said ~~left thumb sensor and a right thumb sensor~~ first sensor and said second sensor of said eight sensors simultaneously exits said ~~delete~~ deletion mode and re-enters said ~~first~~ data entry mode.

32. (original) A method of producing data using at least eight sensors comprising the step of:

shifting into a second mode by entering at least one data character.

33. (currently amended) A method of producing data using at least eight sensors, in accordance with claim 32, comprising the step of:

shifting into a second mode by entering ~~the~~ a language code data character string.

34. (currently amended) A method of producing data using at least eight sensors, in accordance with claim 32, comprising the step of:

shifting into a second mode by entering ~~the~~ a country code data character string.

35. (currently amended) A method of producing data using at least eight sensors, in accordance with claim 32, comprising the step of:

shifting into a second mode by entering ~~the~~ a country's area code data character string.

36. (new) A first four bit code combined with a second four bit code to produce data, in accordance with claim 1, wherein:

an inactive bit of said first four bit code or said second four bit code is represented with a small character and an active bit of said first four bit code or said second four bit code is represented with a large character.